TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

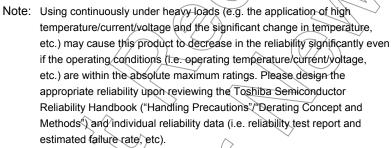
2SK2889

Chopper Regulator, DC–DC Converter and Motor Drive Applications

- Low drain-source ON resistance : $R_{DS (ON)} = 0.54 \Omega$ (typ.)
- High forward transfer admittance $|Y_{fs}| = 9.0 \text{ S} (typ.)$
- Low leakage current $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DSS} = 600 \ V)$
- Enhancement mode : V_{th} = 2.0 to 4.0 V (V_{DS} = 10 V, I_D = 1 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V _{DSS}	600	
Drain-gate voltage (R _{GS} = 20 kΩ)		V _{DGR}	600	(\checkmark)
Gate-source voltage		V _{GSS}	±30	V
Drain current	DC (Note 1)	۱ _D	10	À
	Pulse (Note 1)	I _{DP}	40	ightarrow A
Drain power dissipation (Tc = 25°C)		PD	100	W
Single pulse avalanche energy (Note 2)		Eas <	363	mJ
Avalanche current		IAR	10	A
Repetitive avalanche energy (Note 3)		EAR	10	mJ
Channel temperature		Tch	150	°C
Storage temperature range			-55 to 150	°C



Thermal Characteristics

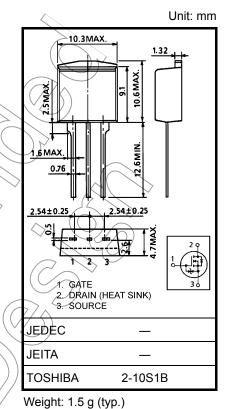
Characteristics Symbol	Max	Unit
Thermal resistance, channel to case Rth (ch-c)	1.25	°C / W
Thermal resistance, channel to ambient Rth (ch-a)	83.3	°C / W

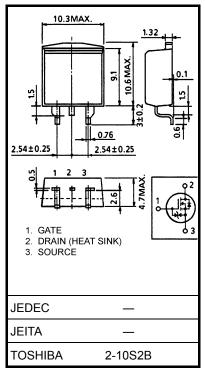
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 6.36 mH, R_G = 25 Ω , I_{AR} = 10 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.





Weight: 1.5 g (typ.)

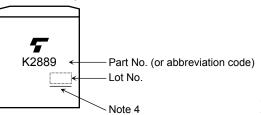
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V_{GS} = ±25 V, V_{DS} = 0 V	_	_	±10	μA
Gate-source br	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V	Ŋ	—	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	600		_	V
Gate threshold	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	-7(4.0	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 5 A		0.54	0.75	Ω
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 5 A	3.0	9.0	_	S
Input capacitance		C _{iss}			2040		
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		230		pF
Output capacitance		C _{oss}		_	590		
Switching time	Rise time	tr	$v_{GS} \stackrel{10V}{}_{0V} \prod I_{D=5A} \stackrel{V_{OUT}}{}_{VOUT}$	- (22	> ¹ $>$	
	Turn-on time	t _{on}	$R_L = 40\Omega$	N N	58) _	ns
	Fall time	t _f		$\langle n \rangle$	> 36		110
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_w = 10\mu s$) –	190	—	
Total gate charg plus gate-drain)		Qg			45		
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ H}_{D} = 10 \text{ A}$	_	25	_	nC
Gate-drain ("miller") Charge		Qgđ		—	20	—	

Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 4)	IDR	_	_	_	10	А
Pulse drain reverse current (Note 1)	I _{DRP}	-	_	_	40	А
Forward voltage (diode)		I _{DR} = 10 A, V _{GS} = 0 V			-1.7	V
Reverse recovery time	trr	I _{DR} = 10 A, V _{GS} = 0 V		1300		ns
Reverse recovery charge	Qrr	dI _{DR} / dt = 100 A / μs		16		nC

Marking

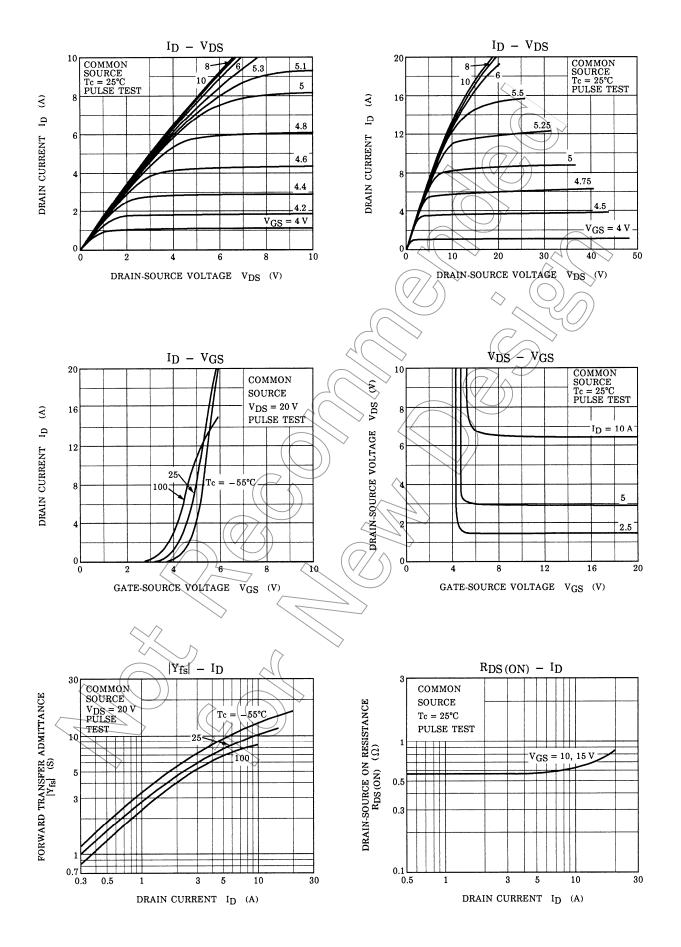


Note 4: A line under a Lot No. identifies the indication of product Labels. Not underlined: [[Pb]]/INCLUDES > MCV

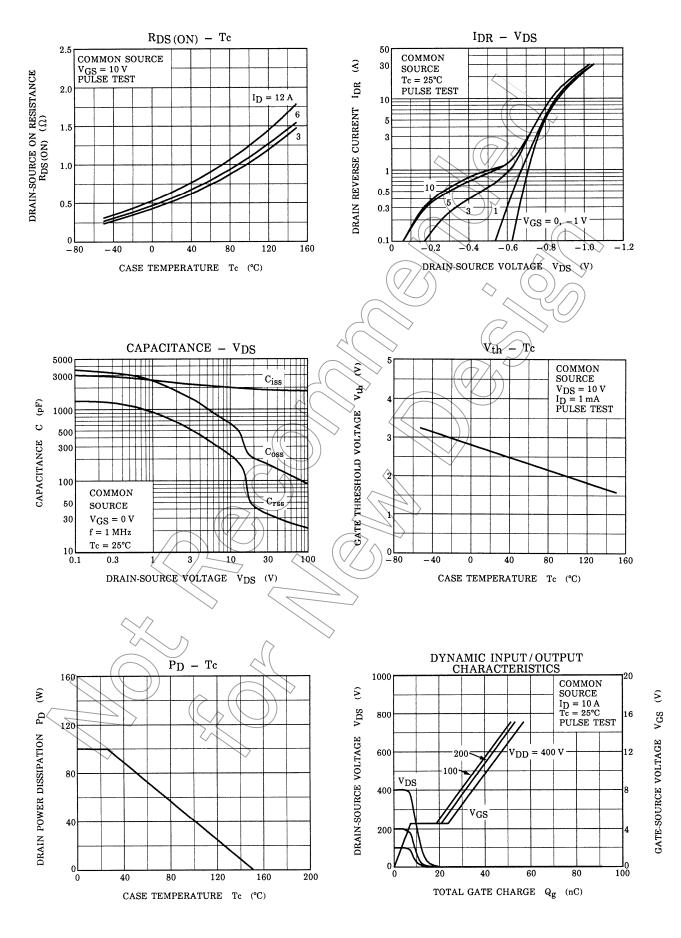
Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

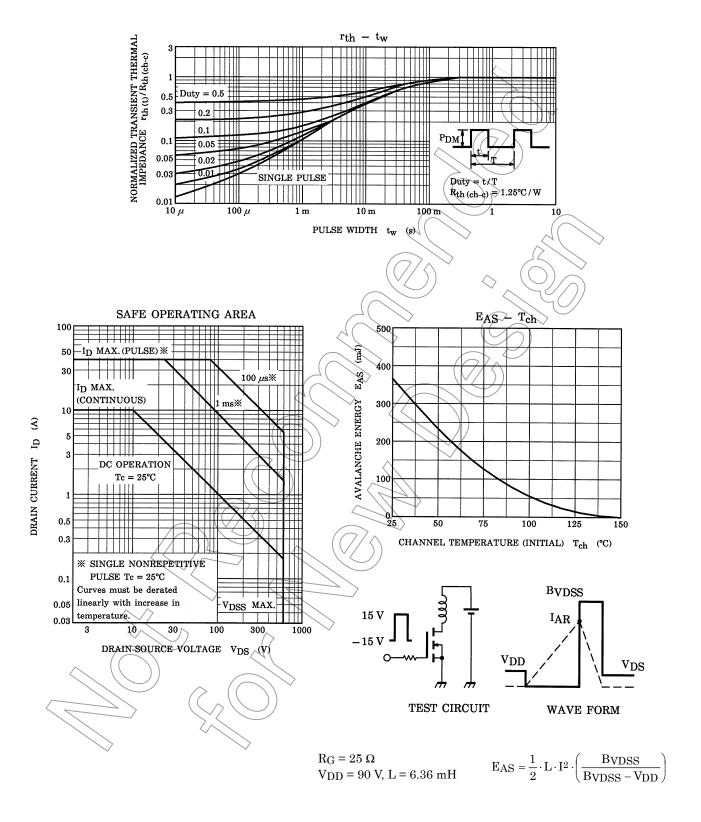
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